

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
••	:	Examiner: S. Isaac
MASATAKA ITO	)	
	:	Group Art Unit: 2812
Application No.: 10/091,461	)	
	:	
Filed: March 7, 2002	)	
	:	
For: SOI SUBSTRATE, ANNEALING	)	
METHOD THEREFOR,	:	
SEMICONDUCTOR DEVICE	)	
HAVING THE SOI SUBSTRATE,	:	
AND METHOD OF	)	
MANUFACTURING THE SAME	:	August 22, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## LETTER TRANSMITTING FORMAL DRAWING

Sir:

Transmitted herewith is a sheet of formal drawings to be substituted for the corresponding drawing sheet presently on file in the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

August 22, 2003 (Date of Deposit)

Chris D. Nguyen (Reg. No. 54,336)

(Name of Attorney for Applicant)

August 22, 2003

nature Date of Signature

Applicant's undersigned attorney may be reached in our Costa Mesa,

California office by telephone at (714) 540-8700. All correspondence should be directed to

our below listed address.

Respectfully submitted,

Attorney for Applicant

Registration No. 59, 336

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